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16. (Currently amended) A bipolar transistor, comprising:

a base;

an emitter contact formed within the base;

a base contact formed within the base; and

a first wiring stack formed atop the emitter contact and a second wiring stack formed atop the base contact, wherein the second wiring stack includes at least one more wiring level than the first wiring stack, wherein the first wiring stack comprises a first contact atop the emitter contact and a first emitter wiring level atop the first contact, and wherein the second wiring stack comprises a second contact atop the base contact, a first base wiring level atop the second contact, a first via containing a first conductive material atop the first base wiring level, and a second base wiring level atop the first via;

a first dielectric layer atop the base, wherein the first dielectric layer comprises the first contact and the second contact;

a second dielectric layer atop the first dielectric layer, wherein the second dielectric layer comprises the first emitter wiring level and the first base wiring level;

a third dielectric layer atop the second dielectric layer, wherein the third dielectric layer

comprises the first via; and

a fourth dielectric layer atop the third dielectric layer, wherein the fourth dielectric layer comprises the second base wiring level.

17. (Original) The bipolar transistor of claim 16, further comprising a collector contact formed within a subcollector.

18. (Original) The bipolar transistor of claim 16, wherein the emitter contact surrounds the base contact on at least two sides.

19. (Original) The bipolar transistor of claim 16, wherein the emitter contact forms a ring around the base contact.

20. (Original) The bipolar transistor of claim 16, wherein the base contact is a point contact.

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32. (New) The bipolar transistor of claim 17, further comprising a third wiring stack formed atop the collector contact, wherein the third wiring stack comprises a third contact atop the collector contact, a first collector wiring level atop the third contact, a second via containing a second conductive material atop the first collector wiring level, and a second collector wiring level atop the second via, wherein the first dielectric layer comprises the third contact, wherein the second dielectric layer comprises the first collector wiring level, wherein the third dielectric layer comprises the second via, and wherein the fourth dielectric layer comprises the second collector wiring level.

33. (New) The bipolar transistor of claim 16, wherein the second wiring stack further comprises a second via containing a second conductive material atop the second base wiring level and a third via containing a third conductive material atop the second via, wherein the bipolar transistor further comprises a fifth dielectric layer comprising the second via and a sixth dielectric layer comprising the third via.

34. (New) The bipolar transistor of claim 16, further comprising a collector contact formed within a subcollector, wherein the collector contact is disposed principally on a single side of the base, and wherein the emitter contact surrounds the base contact.

35. (New) The bipolar transistor of claim 16, further comprising a substrate and a collector, wherein the substrate comprises the base, wherein the base abuts an isolation structure formed in the substrate, wherein the emitter contact at least partially surrounds the base contact, and wherein the collector is disposed below the base and abuts the isolation structure.